



Certificate G59/2

The results of the G59/2 test are summarized in this certificate. DELTA ES declares that all devices (with G59/2 setting) that shipped to the UK comply with the requirements defined in engineering recommendation G59/2 (2010). These settings cannot be changed by an installer without the use of specific software. Test details are available with test report ID: 11TH0120-G59/2_2 (Bureau Veritas).

Hereby tests results for: SOL10.0-1TR3-E4 (EOE47030453)
 SOL11.0-1TR3-E4 (EOE48030469)

1.) Power Quality

Harmonic current emissions (%)								
Harmonic	3rd	5th	7th	9th	11th	13th	THD	PWHD
Limit 1	21.6	10.7	7.2	3.8	3.1	2.0	23	23
Test value	1.434	0.590	0.250	0.153	0.234	0.409	1.90	4.48

1 Maximum permissible harmonics current as per EN 61000-3-12.

Voltage fluctuation and Flicker		
Parameter	Pst	Plt
Un=230V Output power: 100%		
Limit 2	1.0	0.65
Test value	0.35	0.35

2 Maximum permissible voltage fluctuation (expressed as a percentage of nominal voltage at 100% power) and flicker as per BS EN 61000-3-3.

DC Injection				Power factor		
G 59/2 Limit	50 mA (0.25 % In)			0.95 cap – 0.95 ind at three voltages levels		
Test Level	10 %	55 %	100 %	212 V	230 V	248 V
Test value R	-27.0 mA	-48.6 mA	-49.9 mA	0.999	0.999	0.999
Test value S	-29.6 mA	-44.2 mA	-47.5 mA	0.999	0.999	0.999
Test value T	-7.52 mA	13.3 mA	4.53 mA	0.998	0.999	0.999

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2a.) Under / Over Voltage Tests (230 V grid)

	Under voltage		Over voltage	
Parameter	Voltage	Time	Voltage	Time
G 59/2 limit stage 1	-13% Un	2.5 s	+10% Un	1.0 s
Actual settings	200 V	2.5 s	253 V	1.0 s
Trip value	199.6 V	2.462 s	252.5 V	0.964 s
	Under voltage		Over voltage	
Parameter	Voltage	Time	Voltage	Time
G 59/2 limit stage 2	-20% Un	0.5 s	+15% Un	0.5 s
Actual settings	184 V	0.5 s	264 V	0.5 s
Trip Value	N/A	0.455 s	N/A	0.461 s

2b.) Under / Over Voltage Tests (240 V grid)

	Under voltage		Over voltage	
Parameter	Voltage	Time	Voltage	Time
G 59/2 limit stage 1	-13% Un	2.5 s	+10% Un	1.0 s
Actual settings	208 V	2.5 s	264 V	1.0 s
Trip value	208.0 V	2.480 s	264.0 V	0.975 s
	Under voltage		Over voltage	
Parameter	Voltage	Time	Voltage	Time
G 59/2 limit stage 2	-20% Un	0.5 s	+15% Un	0.5 s
Actual settings	192 V	0.5 s	276 V	0.5 s
Trip Value	N/A	0.475 s	N/A	0.470 s

3.) Under / Over Frequency Tests

	Under frequency		Over frequency	
Parameter	Frequency	Time	Frequency	Time
G 59/2 limit stage 1	47.5 Hz	At least 20 s	51.5 Hz	At least 90 s
Actual settings	N/A	N/A	N/A	N/A
Trip Value	N/A	> 20 s	N/A	> 90 s
	Under frequency		Over frequency	
Parameter	Frequency	Time	Frequency	Time
G 59/2 limit stage 2	47.0 Hz	Max 0.5 s	52.0 Hz	Max 0.5 s
Actual settings	47.00 Hz	500 ms	52.00 Hz	500 ms
Trip Value	47.01 Hz	467 ms	52.01 Hz	467 ms

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4.) Loss of Mains tests

Method used	Frequency: 50+/-0.2Hz UN=230+/-3Vac RLC consumes inverter real power within +/- 5% Quality > 2 @ 55% load		
Output Power Level	10%	55%	100%
G 59/2 Limit	5 s		
Actual setting	5000 ms	5000 ms	5000 ms
Trip value	580 ms	589 ms	626 ms

5.) Reconnection Times

Method used	Under/Over voltage	Under/over frequency	Loss of mains
Minimum value	180 s		
Actual setting	180 s	180 s	180 s
Recorded value	183.1 s	182.3 s	182.6 s

6.) Fault Level contribution

-Short circuit Current Contribution

As Photovoltaic SSEGs are inverter connected. they are deemed to automatically comply with regulations and no further tests are required.

-Over Current Protection

The products have to be installed with appropriate protection according to BS7671. See installation manual.

7.) Self monitoring – solid state switching

Units do not provide solid state switching relays. In case the semiconductor bridge is switched off, then the voltage on the output drops to zero. In this case the relays on the output will also open.

Michael Redmann
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